



**Faculty of Technology
University of Delhi**

Placement Brochure

Session 2026-2027

**TOGETHER, WE BUILD THE FUTURE - ONE EQUATION,
ONE CIRCUIT, ONE ALGORITHM AT A TIME.**



Upcoming building



Faculty of Technology, University Of Delhi

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**Nurturing Future
Engineering Leaders**

ABOUT UNIVERSITY OF DELHI



The University of Delhi is one of India's most respected institutions, defined by a century-long record of academic excellence, intellectual leadership, and national impact. Established in 1922, it has consistently set benchmarks in higher education through rigorous scholarship, distinguished faculty, and a deeply interdisciplinary academic environment.

The University has a well-established legacy of imparting technical and engineering education. Renowned technical institutions such as the Delhi Technological University (DTU) and the Netaji Subash University of Delhi (NSUT) have their roots within the University of Delhi's academic framework. Nurtured within the University's storied framework, these institutions have grown into pillars of technical learning in their own right.

The University has consistently been ranked amongst the top universities of India. In the NIRF 2025 rankings, the University secured the fifth position reflecting its academic reputation, research ecosystem and institutional depth. QS world University rankings placed the University at the 328th position, establishing its global recognition. These recognitions are complemented by a strong alumni network that spans notable leadership positions across academia, industry, government, and entrepreneurship. The University is proud to have globally recognized names including Mr. Amitabh Bacchan, Mr. Shah Rukh Khan, Dr. Manmohan Singh, and Mr. Nitin Gadkari as its alumni.

*Guided by its ethos of “**Nishtha Dhriti Satyam**” Dedication, Steadfastness, and Truth the University develops engineers who combine technical depth with clarity of thought, Professional integrity, and execution capability. For global recruiters, the University of Delhi represents a proven foundation of engineering excellence reimaged for the next generation of technology leadership.*

Building upon this well-rooted institutional foundation, the Faculty of Technology represents the University's vision for engineering and technological education in today's rapidly evolving world.

NIRF Ranking(University)-2025: 5

QS(2026) Ranking: 328

QS-Southern Asia(2025) Ranking: 8

16 FACULTIES, 86 ACADEMIC DEPARTMENTS, 91 COLLEGES

132,435 REGULAR STUDENTS (114,494 UG AND 17,941 PG).

Faculty of Technology



100 YEARS OF ENGINEERING EXCELLENCE



1922

The University of Delhi was established, creating the academic foundation that later shaped technical education in Delhi.



1941

Delhi Polytechnic was established, initiating formal engineering education within Delhi's academic ecosystem.



1965

Delhi Polytechnic evolved into Delhi College of Engineering, becoming a major center for technical education.



2009

Delhi College of Engineering transitioned into Delhi Technological University (DTU).



1997

Delhi Institute of Technology was renamed Netaji Subhas Institute of Technology (NSIT).



1983

Delhi Institute of Technology was established, expanding engineering education and strengthening Delhi's technical ecosystem.



2018

NSIT was upgraded to Netaji Subhas University of Technology (NSUT).



2023

The University of Delhi re-established the Faculty of Technology, continuing a long-standing legacy of academic brilliance, innovation, and technical education.

FACULTY OF TECHNOLOGY

Rooted in legacy, Reimagined for the future



The Faculty of Technology, University of Delhi, currently offers four-year B.Tech programs in Computer Science & Engineering (CSE), Electronics & Communication Engineering (ECE), and Electrical Engineering (EE). Designed to align with contemporary technological and industrial needs, the academic framework integrates strong foundational engineering principles with exposure to emerging domains and interdisciplinary applications.

The academic programs are structured to provide students with a strong foundation in their chosen engineering discipline, complemented by carefully curated minors and advanced specialisations. This breadth-and-depth approach equips graduates with the intellectual versatility and technical command demanded by a rapidly evolving professional landscape in today's time.

The curriculum emphasizes laboratory-intensive coursework, project-based learning, and structured capstone projects. Students are encouraged to engage in internships, technical workshops, and applied learning experiences that strengthen practical competencies alongside theoretical rigor.

The first graduating cohort of the Faculty of Technology is expected in 2027. Located in Delhi, the Faculty benefits from proximity to leading technology firms, startups, public sector enterprises, research laboratories, and innovation hubs. This geographic advantage enables structured industry interaction, internship opportunities, and collaborative engagement. Through a carefully designed academic structure and early industry interface, the Faculty of Technology aims to present graduates who are technically competent, analytically strong, and prepared to contribute meaningfully to diverse engineering and technology roles from the outset of their careers.

A Message of Inspiration from the Vice Chancellor



Prof. Yogesh Singh

Vice Chancellor, University of Delhi

The Faculty of Technology at the University of Delhi is committed to advancing excellence in technical education, research, and innovation. As a dynamic and evolving academic unit within one of India's most prestigious universities, the Faculty strives to nurture skilled professionals equipped to meet the demands of a rapidly transforming technological landscape. Our academic programs are thoughtfully designed to provide a strong grounding in fundamental concepts, complemented by interdisciplinary learning, hands-on training, and exposure to emerging technologies. Emphasis is placed not only on technical proficiency but also on critical thinking, problem-solving, and ethical responsibility.

The Faculty actively fosters engagement with industry through collaborative projects, expert interactions, and practical learning opportunities, ensuring that our students remain aligned with real-world expectations. Our dedicated faculty members play a pivotal role in mentoring students and bridging the gap between academic knowledge and industry application through research and innovation. I take this opportunity to invite our esteemed industry partners to engage with the talented and motivated students of the Faculty of Technology. We are confident that our students possess the capability, adaptability, and professionalism required to contribute effectively to your organizations. As we continue to build stronger collaborations between academia and industry, I extend my best wishes to our students for a successful and fulfilling professional journey, and to our industry partners for continued growth and innovation. Together, let us work towards creating a future driven by knowledge, sustainability, and shared progress.

Message from the DEAN'S DESK



Prof. Sanjeev Singh

Dean, Faculty of Technology

It is my privilege to introduce the Faculty of Technology at the University of Delhi and to present our students through this placement brochure. At the Faculty of Technology, we are building a future-oriented engineering ecosystem grounded in academic rigor, innovation, and industry relevance. Our programs are designed to develop graduates who combine strong technical foundations with analytical thinking, adaptability, and a problem-solving mindset. Our approach emphasizes experiential learning and real-world applications. Through laboratory-intensive coursework, interdisciplinary projects, internships, and industry engagement, students are trained to translate knowledge into impactful solutions. They are encouraged to think critically, challenge conventions, and innovate with confidence.

We are simultaneously investing in a robust research and innovation infrastructure, including Centres of Excellence in areas such as next-generation communications, semiconductor technologies, and sustainable energy systems. Supported by advanced laboratories and collaborations with leading industry and research organizations, this ecosystem ensures that our students are exposed to cutting-edge technologies and practical challenges.

As part of the University of Delhi, our students benefit from a rich, interdisciplinary academic environment that fosters both Professional competence and holistic development. We also emphasize on ethics, responsibility, and leadership, preparing graduates to contribute meaningfully to organizations and society.

We deeply value our partnerships with industry and are committed to making your recruitment experience engaging, efficient, and rewarding. I invite organizations across the world to engage with us and explore the opportunities to collaborate and recruit from the Faculty of Technology. We look forward to building enduring global partnerships and contributing to your organization's continued success.

Academic Programs

B.Tech

A four-year undergraduate degree encompassing three core engineering disciplines, designed to provide students with a strong foundation across diverse areas of engineering and technology. A key highlight of the curriculum is the project-driven approach, enabling students to gain practical exposure to modern technologies and current industry practices.

- **B.Tech. in Computer Science & Engineering**
- **B.Tech. in Electronics & Communication Engineering**
- **B.Tech. in Electrical Engineering**

M.Tech

The forthcoming Master of Technology programme will offer advanced interdisciplinary learning with strong emphasis on research, innovation, and industry-oriented applications. The curriculum is designed to balance advanced coursework with hands-on research exposure and collaborative academic engagement.

Ph.D

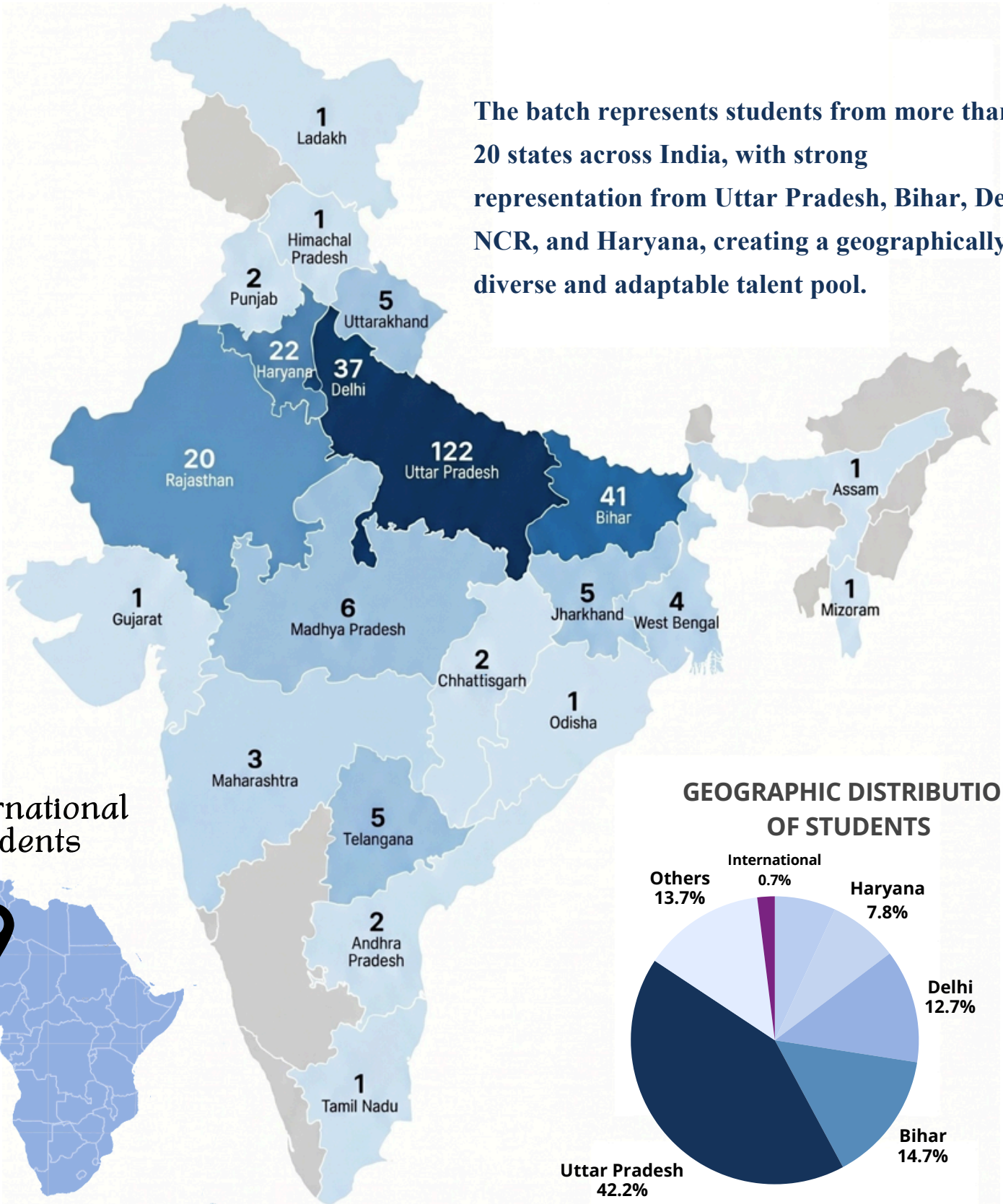
The FoT's upcoming Ph.D. programme will focus on original and impactful doctoral research across emerging and advanced technological domains. The programme is envisioned to promote innovation, scholarly excellence, and research training aligned with evolving scientific, technological, and national developmental priorities.



FACULTY
OF
TECHNOLOGY

Global Diversity at FoT

The batch represents students from more than 20 states across India, with strong representation from Uttar Pradesh, Bihar, Delhi NCR, and Haryana, creating a geographically diverse and adaptable talent pool.



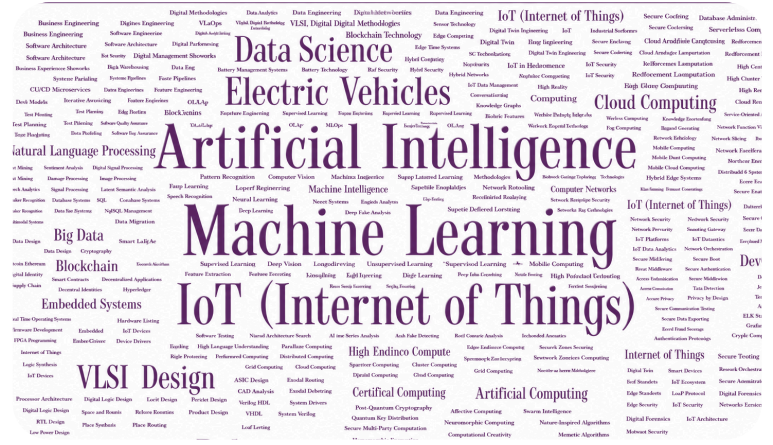
2 International Students



Our inaugural batch represents high-potential talent from over 20 states, rigorously selected through JEE-Mains Exam.

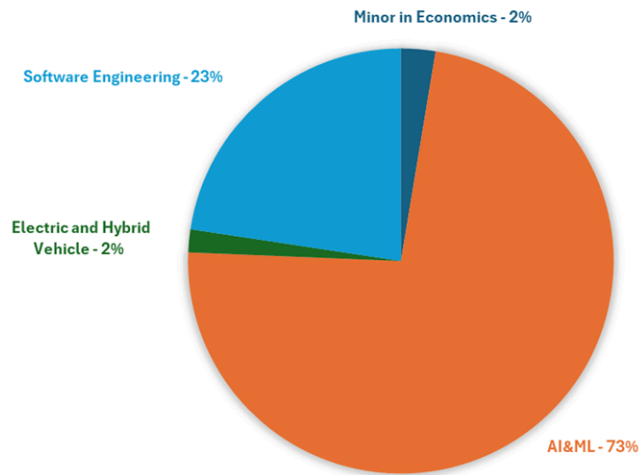
Academic Specialisations at a Glance

Our students pursue interdisciplinary minors and specializations across departments, enabling strong cross-domain expertise spanning software, electronics, AI, embedded systems, and energy technologies. The batch represents a diverse and adaptable talent pool prepared for modern engineering roles

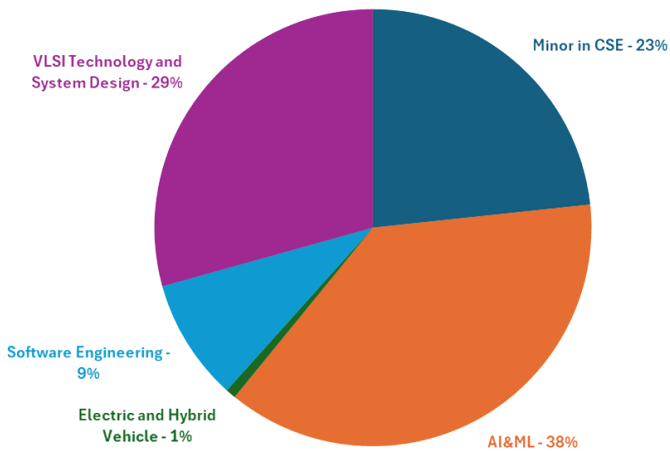


Specialization Distribution

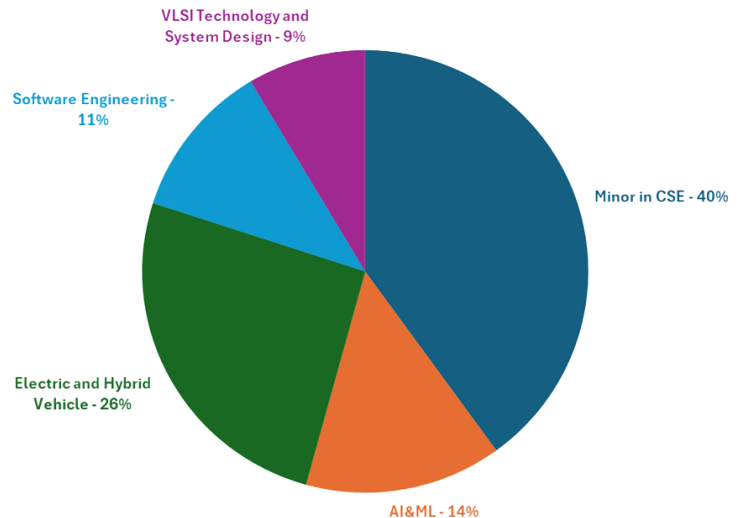
SPECIALIZATION DISTRIBUTION - CSE (2027 BATCH)



SPECIALIZATION DISTRIBUTION - ECE (2027 BATCH)



SPECIALIZATION DISTRIBUTION - EE (2027 BATCH)



Emerging Technology Focus: Interdisciplinary Talent Pool

Academic Cohort Overview

283

Total Candidates

3

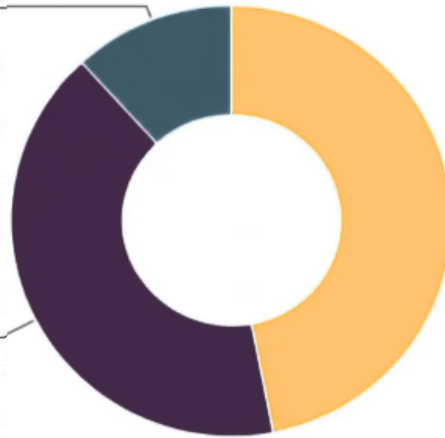
Core Engineering
Disciplines

4

High-Demand Specializations
(AI/ML, VLSI, Software
Engineering, Electric & Hybrid
Vehicles)

Electrical Engineering (EE):
35 Students | 12%

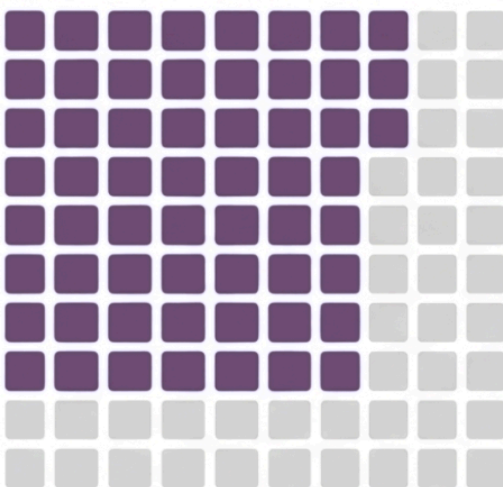
Computer Science & Engineering
(CSE): 115 Students | 41%



Electronics & Communication
Engineering (ECE):
133 Students | 47%

ECE and CSE constitute nearly 90%
of the cohort, providing massive
scale in both hardware systems and
advanced computational software.

CSE Cohort: The AI & Machine Learning Vanguard



84 Students

73% of the entire CSE department is formally
specializing in Artificial Intelligence & Machine
Learning.

The remaining 31 students are distributed across core
Software Engineering and Economics minors, ensuring
strong foundational software development skills.

A highly concentrated, specialized pool of future AI developers, NLP engineers, and data scientists ready for immediate industry deployment.

Talent Pool - Highlights

ECE Cohort: Bridging Next-Gen Hardware and Software

The AI Crossover

50 Students

Specializing in Artificial Intelligence & Machine Learning.

The Hardware Core

39 Students

Specializing in VLSI Technology & System Design.

The Software Bridge

31 Students

Pursuing a formal Minor in Computer Science & Engineering (CSE).

ECE candidates offer dual-threat agility—capable of designing physical VLSI architecture while understanding the computational software it powers.

EE Cohort: Core Systems meets Cross-Skilling

Software Integration

14 Students (40%)

Formally pursuing a Minor in CSE to bridge electrical foundations with modern computational logic.

Emerging Tech

Electric & Hybrid Vehicles **9 Students (26%)**

Dedicated specializations directly targeting the automotive and automation industries.

AI & ML **5 Students (14%)**

Specializing in Artificial Intelligence & Machine Learning.

These diverse specializations demonstrate a highly adaptable talent pool.

EE candidates are actively migrating beyond traditional power systems, acquiring the cross-disciplinary skills required for modern smart-grid and EV innovation.

Unified Capability: 134 Candidates Specializing in AI & ML

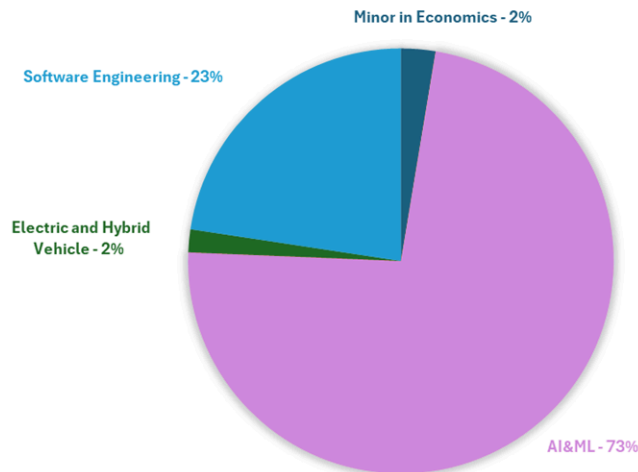
134



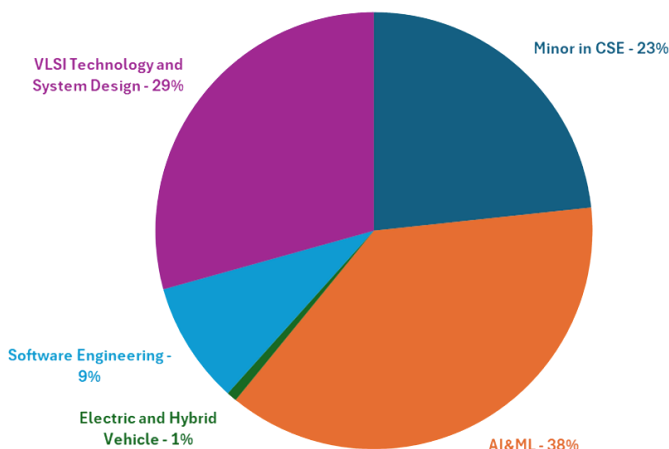
Nearly half of the entire Class of 2026 is formally trained in Artificial Intelligence and Machine Learning. Rather than being siloed in software, this intelligence capability spans both software engineering and hardware communications, representing our largest, most scalable talent pipeline.

Specialization Distribution

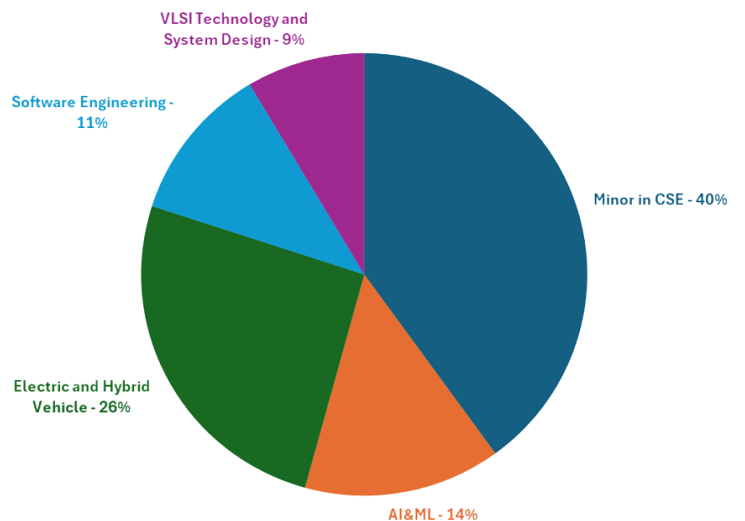
SPECIALIZATION DISTRIBUTION - CSE (2027 BATCH)



SPECIALIZATION DISTRIBUTION - ECE (2027 BATCH)



SPECIALIZATION DISTRIBUTION - EE (2027 BATCH)



Disciplinary Expertise and Career Pathways

- *Many CSE and ECE students specialize in AI/ML, including deep learning, NLP, computer vision, and data analytics.*
- *Students also develop strong skills in software development, full stack technologies, cloud computing, and software engineering through projects and internships.*
- *ECE students gain expertise in VLSI, embedded systems, communication systems, and hardware design.*
- *Electrical Engineering students explore EVs, sustainable energy, power electronics, and automation technologies.*

AI/ML Roles

- AI Engineer
- Machine Learning Engineer
- Data Scientist
- Computer Vision Engineer

Software Engineering Roles

- Software Developer
- Backend Engineer
- Full Stack Developer
- Frontend Engineer
- UI/UX Designer



VLSI & Embedded Systems

- VLSI Design Engineer
- Embedded Systems Engineer
- Hardware Engineer



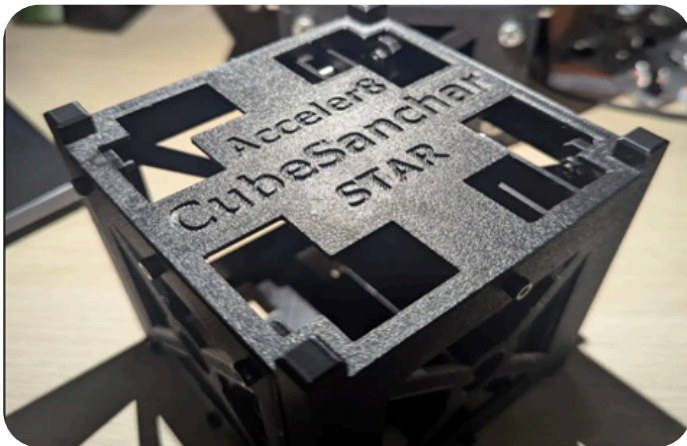
EV & Energy Systems

- EV Systems Engineer
- Power Electronics Engineer
- Energy Systems Analyst



Student Achievements Success Stories

Innovation, Startups & Entrepreneurship



Cubesatellite made by Krishnendra Singh — Winner of Acceler8 Innovation Challenge and recognized at MSME Idea Hackathon 4.0.

National Hackathon Achievements



Finalist at Smart India Hackathon, ranking among the top teams nationally - Shubhika, Rewant, Garvit, Satyam, Krishna & Prayas

Faculty of Technology students excel globally in AI, legal-tech, and intelligent systems, demonstrating world-class technical depth and innovation & contribute actively to advanced engineering research in areas such as communication systems, antenna design, AI, and embedded technologies.

Recognition in AI & Emerging Technologies



Ashish, Minaz, Piyush, Jenny, Saksham — Top 10 Finalist at the Google Cloud GenAI Exchange Hackathon nationwide.

Research Excellence & Publications



Shreyas Singh — Recognized at IEEE MAPCON 2025 for research on high-gain microstrip patch antennas, with work considered for IEEE journal submission.

Global Open-Source Recognition – GSoC 2026



Google
Summer of Code

Shriyam Baloni - Selected for Google Summer of Code 2026 under the Machine Learning for Science (ML4SCI) organization, contributing to the PrediCT project focused on applying machine learning to scientific research problems. This achievement reflects strong expertise in open-source development, machine learning, and research-driven innovation at an international level.

Industry Exposure & Technical Events

Hack4Viksit Bharat



Hack4Viksit Bharat is a flagship hackathon organized by the Faculty of Technology, University of Delhi aligned with the vision of a “Viksit Bharat(Developed India)”. Conducted in collaboration with leading technology and learning platforms such as GeeksforGeeks and supported by industry partners like Google Cloud, the hackathon emphasized real-world application of tools and technologies. Participants worked intensively in teams to build scalable, impactful solutions addressing contemporary challenges across domains

Industry Engagement & Technical Communities

University of Delhi in collaboration with the Faculty of Technology and Delhi University Computer Centre (DUCC), served as a dynamic platform for students to engage with advancements in AI and gaming technologies. Held on October 12, 2025, the event brought together technology enthusiasts, developers, and gamers for an immersive learning and networking experience.



Astraaya 1.0 – University Tech Fest

The Faculty of Technology proudly hosts Astraaya our flagship annual Tech Fest, uniting students across Delhi University in a celebration of innovation, collaboration, and creativity. The fest featured engaging competitions such as AI War, Line Following Robot, Ideathon, and DSA Challenge, offering participants a platform to demonstrate creativity, problem-solving, and technical expertise.



IEEE-EDS Summer School on Semiconductor

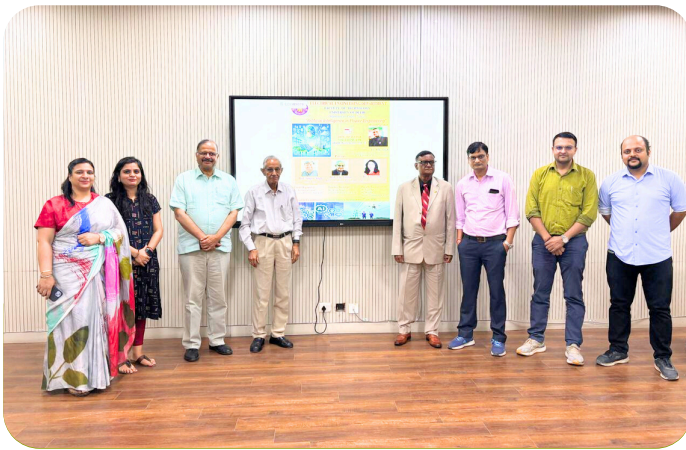
The IEEE Electron Device Society (EDS) Summer School 2025, hosted in part at the Faculty of Technology, University of Delhi, provided a rigorous academic platform for learning advanced semiconductor technologies. Organized in collaboration with leading research institutions such as SSPL and NPL, the program focused on device fabrication, characterization, and emerging applications.



2025 IEEE ELECTRON DEVICE SOCIETY (EDS) SUMMER SCHOOL			
FABRICATION, CHARACTERIZATION OF III-N DEVICES AND SYSTEMS AND NOVEL APPROACH FOR SOLID STATE LIGHTING			
ABSTRACT Field Effect Transistors (FETs), including MOSFETs and HEMTs, are the backbone of modern electronics, enabling applications from consumer products to high-performance communication and computing systems. As we push FETs to their limits, this Summer School offers a platform to explore the evolution and future of transistor technology. The program brings together experts to discuss advances in fabrication, characterization, and device applications, with emphasis on III-Nitride materials and wide bandgap technologies. Participants will gain theoretical insights into device physics alongside hands-on training in semiconductor processing, RF circuit design, and emerging quantum technologies.	THEME  The school focuses on the design, optimization, and simulation of FETs, MOSFETs, and HEMTs. Topics cover III-Nitride device physics, microphotography, and wide-bandgap electronics, providing participants with both theoretical depth and practical exposure. Highlights <ul style="list-style-type: none"> Semiconductor device scaling & technology roadmap Structural & optical characterization of III-Nitride heterostructures Fundamentals of device physics: transport, bandgap engineering Advances in GaAs/GaN HEMT fabrication Applications in RF, optoelectronics, and power electronics 	TECHNICAL PROGRAM Day 1: GaN growth & heterostructure tech (PrT), followed by lab session (SSPL) Day 2: GaN/optoelectronic devices & RF circuits (SSPL) followed by lab session (SSPL) Day 3: RF assembly & power electronics (PrT), quiz (SSPL), followed by lab session (SSPL) Day 4: Sessions by NPL Scientists (NPL) Day 5: Sessions by NPL Scientists (NPL)	ABOUT THE EVENT Dates: October 06–10, 2025 Venue: Delhi, India • Lab Sessions and Tutorials only for shortlisted candidates (notified through email) • Lecture Sessions on October 6 and October 8 are free to attend for all. REGISTRARS Dean Deyal Updhyaya College, IEEE EDS Delhi Chapter, Faculty of Technology, University of Delhi, Delhi-110007
IMPACT The Summer School equips participants with skills in advanced device fabrication, characterization techniques, and exposure to cutting-edge fields like quantum computing. It strengthens the bridge between academic research and industry needs.	STEERING COMMITTEE Prof. Manoj Saxena Chapter Chair IEEE EDS Delhi Chapter Dean Deyal Updhyaya College Prof. Sanjay Singh Prof. Sudhakar Verma Dean, Faculty of Technology, University of Delhi	SPEAKERS Dr. Kamal Lohani / Dr. Kapil Narsing Heterostructure Growth (Scientist F) Dr. Abhishek Pandey Semiconductor Characterization (Scientist F) Dr. Robert Latham GaN HEMT Technology (Scientist F) Dr. Neeljit Jain High-Current Diode Lasers (Scientist F) Dr. Anurag Goyal RF Microwave Circuits (Scientist F) Dr. Sandip Kumar Tanna High-Frequency RF Measurement (Scientist - Y) Prof. Ganesh Kumar Chaudhry Power Electronics (Scientist F) Dr. Anurag Kumar Nanomaterials Solar Cells (Chf/Scientist, NPL) Dr. Anshu Chandra Quantum Computing (Days of Physics, IIT Delhi) Prof. Ajay Junction & Thermoelectric Transport (IIT Bombay)	COORDINATORS Dr. Khushwant Sehra Department of Electronics and Communication Engineering Dr. Arjun Tyagi Department of Electrical Engineering Mr. Umesh Shukla Department of Computer Science and Engineering

Expert Talks, Workshops & Industry Interactions

Faculty of Technologyu regularly hosts expert lectures, international research talks, and industry interaction sessions to expose students to emerging technologies, global research opportunities, and career pathways. Sessions have covered domains including Artificial Intelligence, MEMS, biomedical technologies, semiconductor systems, sustainability, and placements.



"Artificial Intelligence in Power Engineering"
by Er. R.K. Singh, Former Engineer-in-Chief, Door Darshan ICT& Broadcast Media Consultant



"Cybersecurity Awareness Session"
Deepak Kumar, Dy. Commandant, CRPF



"Cybersecurity Awareness Session"
Deepak Kumar, Dy. Commandant, CRPF
Dr. Deepak Kumar, Senior Digital Forensics Cyber Intelligence Professional



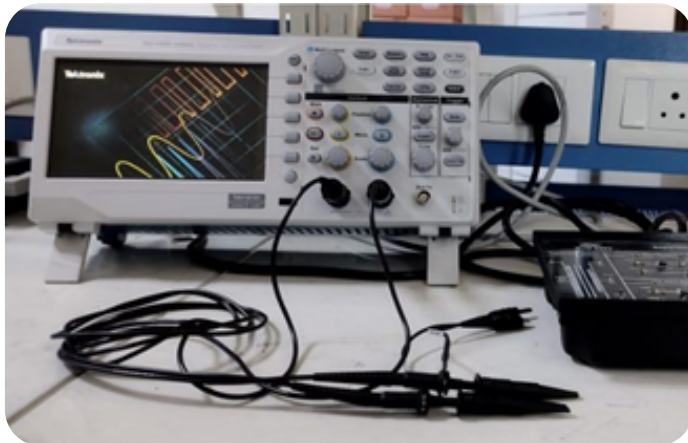
"Energy Futures Solutions: Terrestrial Applications and space relevance"
Dr. N.D. Kaushika: Formar Professor, IIIT Delhi

Laboratories & Infrastructure

FOT has the distinction of being the only institute in India, providing a heterogeneous environment comprising of three major platforms. A unique feature of the institute is that all the labs are managed by the students & faculties of FOT.

Electrical and Electronics Laboratory

The Electrical and Electronics Laboratory is a facility for learning and experimenting with electrical and electronic circuits. It is equipped with tools like oscilloscopes, multimeters, function generators, power supplies, transformers, and DC motors. Students, especially in the first year, use it to design and analyze circuits and understand concepts such as Ohm's Law, Kirchhoff's laws, and semiconductor behavior. The lab promotes hands-on learning and helps develop practical and troubleshooting skills.



Electrical Laboratory

Under NEP, skill development is emphasized for B.Tech students from the beginning. Electrical engineering workshops are conducted throughout the four-year course. In the first year, students learn basic trades like carpentry, sheet metal work, and fitting, along with electrical tasks such as house wiring, meter installation, MCBs, and handling household devices. In later semesters, they are trained in motor overhauling, rewinding, and control panel design. The workshops provide hands-on experience and promote practical skills development.



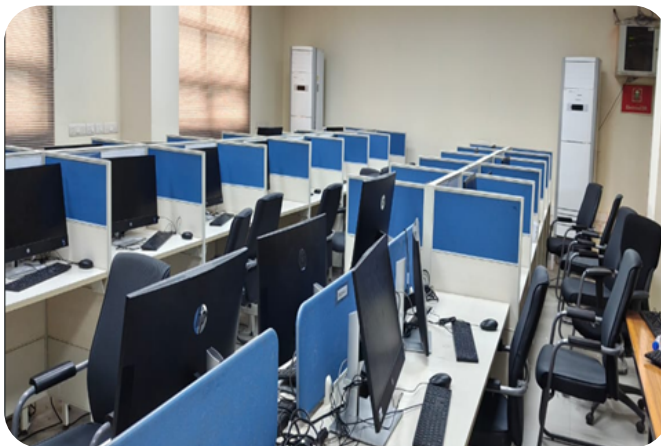
Power Transmission Laboratory

This laboratory supports key power systems experiments on transmission lines, distribution networks, and insulation testing. It includes setups for analyzing line parameters, Ferranti effect, and DC distribution systems. Students perform experiments on insulators, cable capacitance, voltage drops, and distribution network performance, including load types, efficiency, faults, and power factor improvement.

A transformer oil testing setup is also available. The lab enhances practical skills in system analysis, fault handling, and efficiency optimization.



Control System Laboratory



The Control Systems Laboratory helps students understand dynamic systems and modern control techniques through both practical and simulation-based learning. It includes experiments with AC and DC servo motors, enabling students to study system modeling, parameter identification, and controller design like PID. MATLAB-based simulations further enhance analysis and design skills. The lab prepares students for real-world applications in automation, robotics, and control engineering.

Software Subscriptions

Access of Google Apps through Official Google Account	Silvaco
E-resources through E-library	Multisim
MS Office	CST Studio
Turnitin	Cadence Virtuoso
Mathematica	Keysight ADS
Adobe Creative Suite	MATLAB
	LabVIEW



The Central Science Library (CSL) is a specialized library located within the Faculty of Science at the University of Delhi in Delhi, India. It serves as a key resource for science students and faculty, providing access to academic materials.



The complex is managed by the Delhi University Sports Council (DUSC) and serves as the central hub for athletic activities, having hosted venues for the 2010 Commonwealth Games.

Internships Done by FoT Students

FoT students have secured internships and research opportunities at leading organizations, research institutions, and government laboratories, reflecting strong technical competence and industry readiness. Several students have been selected for prestigious programs such as the Vice Chancellor's Internship Scheme (VCIS) and research internships at DRDO labs, IITs, NITs, IISERs, and research centers including IASc-INSA-NASI-CSIO-CSIR.

Students have gained experience across AI/ML, analytics, software development, embedded systems, and research-driven engineering through organizations such as Agrosoft Solutions Pvt. Ltd., Hindustan Power Projects Pvt. Ltd., Keshavam Krati Services Pvt. Ltd., The Data Guy, and the Aspire Leaders Program by Harvard University.

Training & Placement Cell

The Training & Placement Cell facilitates internships, placements, recruiter engagement, and career guidance activities for students. The cell supports organizations throughout the recruitment process while ensuring smooth coordination between recruiters and students.

Recruitment Support

- Pre-placement coordination
- Internship & placement facilitation
- Student-recruiter communication
- Scheduling & assessment support

Contact Us



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<https://sites.google.com/view/sangeetayadav/home>



Dr. Ahmad Zaman Khan

Additional Training Placement Officer

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Student Placement Team



Ronit Jaiswal

STUDENT COORDINATOR



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STUDENT COORDINATOR



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STUDENT COORDINATOR



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